Appl. No. 10/717,341
Amdt. dated January 11, 2008
Reply to Office Action of October 18, 2007

PATENT

Amendments to the Specification:

Please replace the paragraph at page 6, lines 6-23 (paragraph [0025] of the published application) with the following amended paragraph, in which added text is indicated by underlining, deleted text is indicated by strikethrough, and changes are identified by a change bar in the margin:

Next, the mechanisms as shown above are explained in detail. FIG. 3 shows in more detail the structure of the client disk array device 10 and the remote disk array device 20 which are operated as above. These disk array devices 10, 20 are structured by comprising: the plurality of disk units 1a, 2a actually installed on the insertion slots respectively, host interfaces 1b, 2b and channel adapters (CHA) 1c, 2c for performing communication and I/O control with host computers H1, H2 which access the disk array devices 10, 20, cash cache memories 1d, 2d which function as data buffers at the time of write-in, disk adapters (DKA) 1e, 2e for staging/de-staging to the cash memory the write-in object data to the disk unit, common memories 1f, 2f structured by such as ROM and RAM, communication interfaces 1g, 2g for controlling communication by the private line 30, terminal interfaces 1h, 2h performing communication control with management terminals C1, C2 which perform monitoring of various operation settings and working states of the respective disk array devices 10, 20, and service processors 1j, 2j performing unification control of the respective disk array devices 10, 20 and execution control of such as an OS (operating system) which is operated thereon.

Please replace the paragraph at page 11, lines 6-20 (paragraph [0039] of the published application) with the following amended paragraph, in which added text is indicated by underlining, deleted text is indicated by strikethrough, and changes are identified by a change bar in the margin::

In regards to the explanation of the read-write process of data to the above mentioned remote unit 2a, the movement of the staging or de-staging to the cash cache memory of the data performed by the disk adapter (DKA) 1e is not mentioned, but in actuality, at the time of the read-write process of data to the remote unit 2a, these processes appropriately exist. That is, when the object data is staged in the cash cache memory 1d at the time of read-out of the data,

the client disk array device 10 does not perform the read-out process from the remote unit 2a, but uses the data on the ~~cash~~ cache memory 1d, and performs the read-out process from the remote unit 2a only when the object data is not staged. Further, at the time of write-in of the data to the remote unit 2a, the client disk array device 10 once stages the write-in object data to the ~~cash~~ cache memory 1d, then performs the write-in process to the remote unit 2a at an appropriate opportunity, such as a de-staging timing such as shutdown of the client disk array device 10, or when the total of the sizes of the staged write-in object data exceeds the predetermined amount.